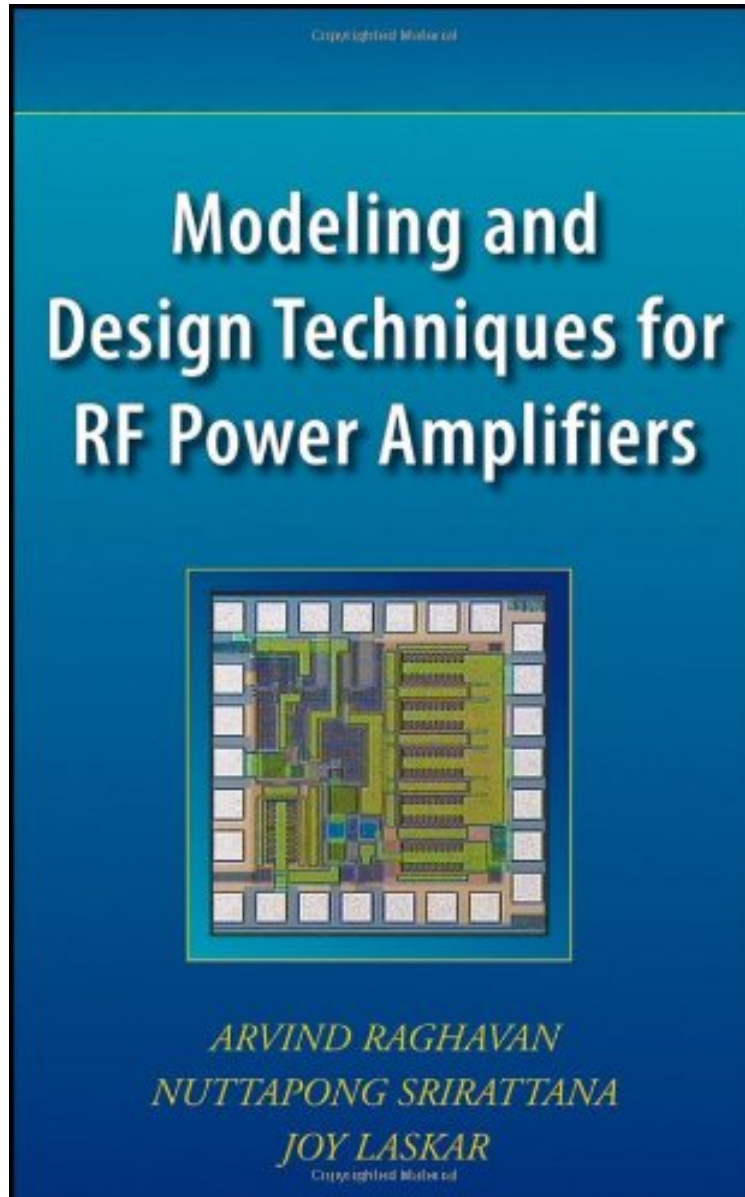


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# Modeling and Design Techniques for RF Power Amplifiers

*Arvind Raghavan, Nuttapong Srirattana, Joy Laskar*

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**Arvind Raghavan, Nuttapong Srirattana, Joy Laskar : Modeling and Design Techniques for RF Power Amplifiers** before purchasing it in order to gage whether or not it would be worth my time, and all praised Modeling and Design Techniques for RF Power Amplifiers:

Achieve higher levels of performance, integration, compactness, and cost-effectiveness in the design and modeling of radio-frequency (RF) power amplifiers. RF power amplifiers are important components of any wireless transmitter, but are often the limiting factors in achieving better performance and lower cost in a wireless communication system, presenting the RF IC design community with many challenges. The next-generation technological advances presented in this book are the result of cutting-edge research in the area of large-signal device modeling and RF power amplifier design at the Georgia Institute of Technology, and have the potential to significantly address issues of performance and cost-effectiveness in this area. Richly complemented with hundreds of figures and equations, *Modeling and Design Techniques for RF Power Amplifiers* introduces and explores the most important topics related to RF power amplifier design under one concise cover. With a focus on efficiency enhancement techniques and the latest advances in the field, coverage includes: Device modeling for CAD Empirical modeling of bipolar devices Scalable modeling of RF MOSFETs Power amplifier IC design Power amplifier design in silicon Efficiency enhancement of RF power amplifiers The description of state-of-the-art techniques makes this book a valuable and handy reference for practicing engineers and researchers, while the breadth of coverage makes it an ideal text for graduate- and advanced undergraduate-level courses in the area of RF power amplifier design and modeling.

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About the Author Arvind Raghavan is a Senior Design Engineer for the Intel Corporation. Nuttapong Srirattana is a Senior Design Engineer at RF Micro Devices. Joy Laskar holds the Schlumberger Chair in Microelectronics and is the founder and Director of the Georgia Electronic Design Center within the School of Electrical and Computer Engineering at the Georgia Institute of Technology.